

Electronic Acknowledgement Receipt

EFS ID:	4088132
Application Number:	10748906
International Application Number:	
Confirmation Number:	7419
Title of Invention:	SEMICONDUCTOR MEMORY DEVICE TESTABLE WITH A SINGLE DATA RATE AND/OR DUAL DATA RATE PATTERN IN A MERGED DATA INPUT/OUTPUT PIN TEST MODE
First Named Inventor/Applicant Name:	Jong-Cheol Lee
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Attorney Docket Number:	8836-223JHM/ID12244US
Receipt Date:	09-OCT-2008
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	ISSUE-FEE-TRANS-8836-223.pdf	197910 aeacfe0d08a4e06650c1506313b5ac0699b3d3ed	no	1

Warnings:**Information:**

2	Fee Worksheet (PTO-06)	fee-info.pdf	34097 6d7c20ecb3a7570d3b767bd6529799bfa418c4a6	no	2
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Warnings:**Information:**

Total Files Size (in bytes):			232007
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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